



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re. Application of: REEVES, M. ET AL.
Serial No.: 09/978,276
Filed: October 17, 2001
Title: SYSTEM AND METHOD FOR ESTABLISHING A COMMUNICATION
PATH ASSOCIATED WITH AN MPLS IMPLEMENTATION ON AN ATM
PLATFORM
Examiner: not yet assigned
Art Unit: 2661
Atty's Docket No.: 123081-339756 (T01215-0071-US)

Date: June 27, 2005

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Dear Sirs:

In accordance with 37 C.F.R. § 1.56, the Applicant encloses a completed Information Disclosure Statement. Submitted contemporaneously herewith is a Notice of Change of Correspondence Address.

Applicant believes that this statement is filed before the mailing of a first Office action on the merits, and as such, no fee is believed to be required pursuant to 37 C.F.R. § 1.97 (b). However, in the event that a first Office action on the merits have been mailed prior to the filing of this statement, or if filing fees are deemed to be due, the Commissioner is hereby authorized

to charge any such fee to deposit account no. 15-0633 of McCarthy Tétrault LLP. Applicant encloses a blank form PTO/SB/17 for payment of any such fee, if necessary.

According to the terms of 37 C.F.R. § 1.97(g) and (h), this Information Disclosure Statement shall not be construed as a representation that a search has been made, an admission that the information cited is or is considered to be material to patentability, or that no other material information exists.

Two (2) United States patent documents, one (1) foreign patent document and three (3) non-patent documents are cited on the enclosed two (2) sheets of Form PTO/SB/08. A copy of each cited foreign patent document and non-patent document is enclosed in accordance with 37 C.F.R. § 1.98.

The foregoing Information Disclosure Statement is based upon the information contained in the undersigned attorney's file as of the filing date of this statement and is inclusive of the best information known to the undersigned at that date. Prompt consideration of this Information Disclosure Statement and the references by the Examiner is respectfully requested.

Should any Patent Office official want to telephone, the call should be made to Robert N. Nakano (Registration No. 46,498) at (416) 601-7852.

Yours very truly,

A handwritten signature in dark ink, appearing to be 'R. Nakano', followed by a long horizontal line extending to the right.

Robert H. Nakano
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet 2 of 2

Complete if Known

Application Number	09/978,276
Filing Date	October 17, 2001
First Named Inventor	REEVES, M. et al.
Group Art Unit	2661
Examiner Name	not yet assigned
Attorney Docket Number	123081-339756

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		LE FAUCHEUR, Francois "IETF Multiprotocol Label Switching (MPLS) Architecture" IEEE International Conference on ATM, June 22, 1998, pages 6-15, XP-002115225	
		ARMITAGE, Grenville "MPLS: The Magic Behind the Myths" IEEE Communications Magazine, January 2000, pages 124-131, XP-000908346	
		REKHTER Y et al., "CISCO SYSTEMS' TAG SWITCHING ARCHITECTURE OVERVIEW", Network Working Group, February 1997, pages 1-12, XP-002111745	

Examiner
SignatureDate
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